EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((wiring adj substrate) and (memory adj chip) and (rectangular adj shape) and (bonding adj terminals) and (microcomputer adj chip) and square and (bonding adj terminals) and (first adj wires) and (second adj wires)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/27 00:01
L2	0	((wiring near2 substrate) and (memory near2 chip) and (rectangular near2 shape) and (bonding adj terminals) and (microcomputer near2 chip) and square and (bonding adj terminals) and (first adj wires) and (second adj wires)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/27 00:02

10/27/08 12:03:13 AM
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